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( Not for submission under 37 CFR 1.99)

Application Number	10629301
Filing Date	2003-07-28
First Named Inventor	Lewis B. Aronson
Art Unit	2613
Examiner Name	Kenneth J. Malkowski
Attorney Docket Number	15436.247.2.1.3

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1	GREEN, MICHAEL, et al., "Mixing it up with OC-192 CMOS Transceivers", CommsDesign, An EE Times Community, 8 pgs, 03 May 2006, available at: <a href="http://www.commsdesign.com/main/2000/12/0012feat6.htm">http://www.commsdesign.com/main/2000/12/0012feat6.htm</a>	<input type="checkbox"/>
2	Dallas Semiconductor, "HIFTA-01.0: 2.5Gbps SDH/SONET Fiber Optic Chipset," 4 pages October 10, 2000, available at: <a href="http://www.maxim-ic.com/appnotes.cfm/appnote_number/713/1n/en">http://www.maxim-ic.com/appnotes.cfm/appnote_number/713/1n/en</a>	<input type="checkbox"/>
3	Dallas Semiconductor, "MAX3872 Multirate Clock and Data Recovery with Limiting Amplifier," 2 pages, March 30, 2003, available at: <a href="http://www.maxim-ic.com/quick_view2.cfm/qv_pk/3705">http://www.maxim-ic.com/quick_view2.cfm/qv_pk/3705</a>	<input type="checkbox"/>
4	Huiqing Zhong and Stephen I. Long, "Monolithic Clock and Data Recovery Chip for 10GB/S Fiber Communications Systems," 4 pages 1997-98.	<input type="checkbox"/>
5	GIGA an Intel Company, "2.5 Gbit/s Clock and Data Recovery GD16546B," 9 pages, July 31, 2001.	<input type="checkbox"/>

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- See attached certification statement.  
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

None

**SIGNATURE**

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Peter F. Malen, Jr./	Date (YYYY-MM-DD)	2007-06-29
Name/Print	Peter F. Malen, Jr.	Registration Number	45576

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